/Alex A. Courtade/

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Robert M. Zeidman Atty.Dkt.No.: 6257-16302 Inventor: Ş § Serial Number: 10/044,217 Š Examiner: Luu, Cuong V. § § Filing Date: November 19, 2001 Group/Art Unit: 2128 Conf. No.: 9153 Title: SYSTEM AND METHOD FOR CONNECTING A LOGIC § CIRCUIT SIMULATION TO A \$ \$ \$ \$ ****CERTIFICATE OF E-FILING TRANSMISSION**** NETWORK I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below: Š

§ RESPONSE TO FINAL OFFICE ACTION MAILED FEBRUARY 5, 2010

On: June 7, 2010

Date

This paper is submitted in response to a Final Office Action of February 5, 2010, to further highlight why the application is in condition for allowance.

Please amend the claims as indicated below